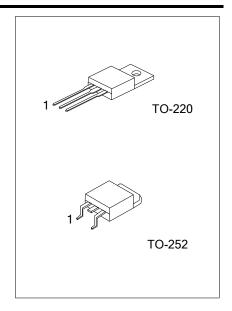
# **MJE2955T**

# PNP SILICON TRANSISTOR

# **HIGH VOLTAGE TRANSISTOR**

#### **DESCRIPTION**

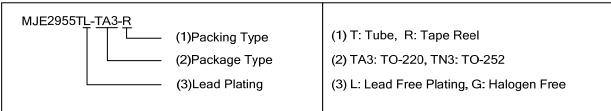
The UTC MJE2955T is designed for general purpose of amplifier and switching applications.



#### **ORDERING INFORMATION**

Ordering Number		Daakaaa	Pin Assignment			Do okio o	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MJE2955TL-TA3-T	MJE2955TG-TA3-T	TO-220	В	C	E	Tube	
MJE2955TL-TN3-R	MJE2955TG-TN3-R	TO-252	В	С	E	Tape Reel	

Note: B:BASE C: COLLECTOR E: EMITTER



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# ■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	$V_{CBO}$	-70	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-60	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector current	Ic	-10	Α
Base Current	I <sub>B</sub>	-6	Α
Total Power Dissipation (T <sub>A</sub> =25°C)	Pc	75	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

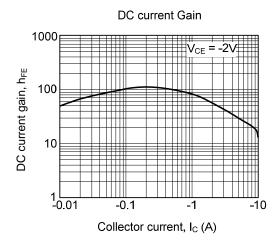
Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

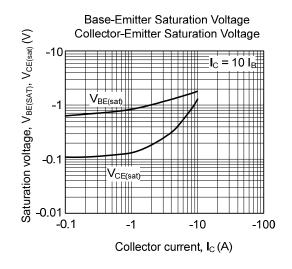
# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

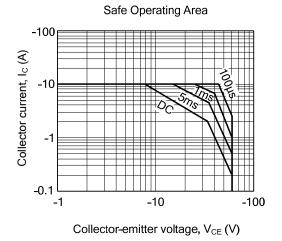
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =-200mA	-60			V	
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =-10mA	-70			V	
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-10mA	-5			V	
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-70V			-1	mΑ	
	I <sub>CEO</sub>	V <sub>CE</sub> =-30V			-700	μΑ	
	I <sub>CEX</sub>	V <sub>CE</sub> =-70V, V <sub>EB(OFF)</sub> =-1.5V			-1	mΑ	
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> =-5V			-5	mΑ	
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)1</sub>	I <sub>C</sub> =-4A, I <sub>B</sub> =-0.4A			-1.1	V	
	V <sub>CE(SAT)2</sub>	I <sub>C</sub> =-10A, I <sub>B</sub> =-3.3A			-8.0		
Baser-Emitter on Voltage	V <sub>BE(ON)</sub>	$V_{CE}$ =-4V, $I_{C}$ =-4A			-1.8	V	
DC Commont Coin	h <sub>FE1</sub>	I <sub>C</sub> =-4A, V <sub>CE</sub> =-4V	20		100		
DC Current Gain	h <sub>FE2</sub>	I <sub>C</sub> =-10A, V <sub>CE</sub> =-4V	5				
Current Gain Bandwidth Product	f⊤	V <sub>CE</sub> =-10V, I <sub>C</sub> =-0.5A, f=1MHz	2			MHZ	

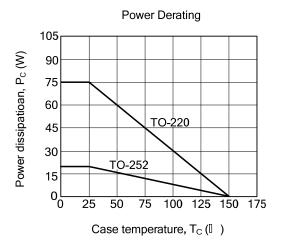
<sup>2.</sup> The device is guaranteed to meet performance specification within  $0^{\circ}\text{C} \sim 70^{\circ}\text{C}$ 

# **■ TYPICAL CHARACTERISTICS**









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