

Vishay General Semiconductor

Glass Passivated Junction Rectifier

Major Ratings and Characteristics

I _{F(AV)}	1.0 A
V _{RRM}	200 V to 800 V
I _{FSM}	50 A
I _R	5.0 μΑ
V _F	1.2 V
T _j max.	175 °C



* Glass-plastic encapsulation technique is covered by Patent No. 3,996,602, and brazed-lead assembly by Patent No. 3,930,306

DO-204AC (DO-15)

Features

- Superectifier structure for High Reliability application
- · Cavity-free glass-passivated junction
- · Low forward voltage drop
- · Low leakage current
- High forward surge capability
- Meets environmental standard MIL-S-19500
- Solder Dip 260 °C, 40 seconds

Mechanical Data

Case: DO-204AC, molded epoxy over glass body Epoxy meets UL-94V-0 Flammability rating

Terminals: Matte tin plated leads, solderable per

J-STD-002B and JESD22-B102D

E3 suffix for commercial grade, HE3 suffix for high reliability grade (AEC Q101 qualified)

Polarity: Color band denotes cathode end

CHAAI

Typical Applications

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes application

Maximum Ratings

(T_A = 25 °C unless otherwise noted)

Parameter		1N5059GP	1N5060GP	1N5061GP	1N5062GP	Unit
* Maximum repetitive peak reverse voltage		200	400	600	800	V
Maximum RMS voltage		140	280	420	560	V
* Maximum DC blocking voltage		200	400	600	800	V
* Maximum average forward rectified current 0.375" (9.5 mm) lead length at T $_{A} = 75\ ^{\circ}\text{C}$	I _{F(AV)}	1.0			Α	
* Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50			Α	
* Maximum full load reverse current, full cycle $$\rm T_A=25~^{\circ}C$$ average 0.375" (9.5 mm) lead length at $$\rm T_A=75~^{\circ}C$$	(,,	5.0 150			μΑ	
Operating junction and storage temperature range		- 65 to + 175				°C

Document Number 88513 www.vishay.com 17-Oct-05 1

1N5059GP thru 1N5062GP

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Electrical Characteristics

(T_A = 25 °C unless otherwise noted)

Parameter	Test condition	Symbol	1N5059GP	1N5060GP	1N5061GP	1N5062GP	Unit
* Max. instantaneous forward voltage	at 1.0 A, T _A = 75 °C	V _F	1.2				٧
* Maximum DC reverse current at rated DC blocking voltage	T _A = 25 °C T _A = 175 C	I _R	5.0 300				μΑ
Typical reverse recovery time	at $I_F = 0.5 A$, $I_R = 1.0 A$, $I_{rr} = 0.25 A$	t _{rr}	2.0			μs	
Typical junction capacitance	at 4.0 V, 1 MHz	СЈ		1	5		pF

Thermal Characteristics

(T_A = 25 °C unless otherwise noted)

Parameter	Symbol	1N5059GP	1N5060GP	1N5061GP	1N5062GP	Unit
Typical thermal resistance(1)	$R_{\theta JA}$	45			°C/W	
	$R_{\theta JL}$	20				

Notes:

(1) Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length, P.C.B. mounted

Ratings and Characteristics Curves

(T_A = 25 °C unless otherwise noted)

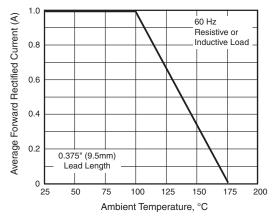


Figure 1. Forward Current Derating Curve

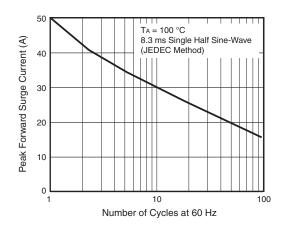


Figure 2. Maximum Non-repetitive Peak Forward Surge Current

^{*}JEDEC registered values



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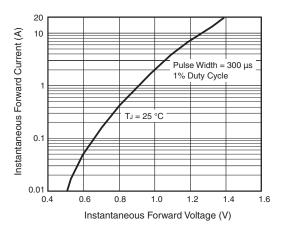


Figure 3. Typical Instantaneous Forward Characteristics

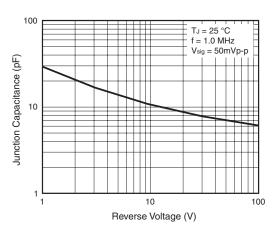


Figure 5. Typical Junction Capacitance

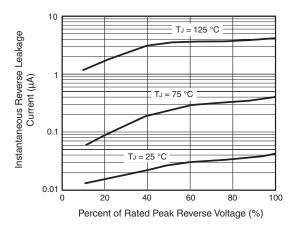


Figure 4. Typical Reverse Characteristics

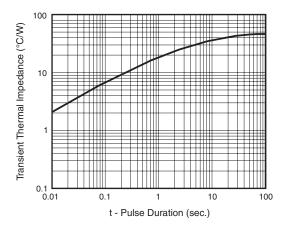
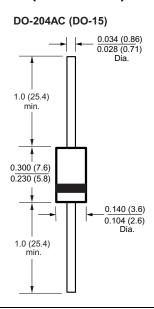


Figure 6. Typical Transient Thermal Impedance

Package outline dimensions in inches (millimeters)



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www.vishay.com Revision: 08-Apr-05