Semiconductors

Linear I.C.'s - Operational Amplifiers

LM 101A, 201A, 301A, General Purpose Operational Amplifiers

GENERAL DESCRIPTION

The 101 A and 301 A are General Purpose monolithic Operational Amplifiers constructed using the Fairchild Planar epitaxial process. These integrated circuits are intended for applications requiring low input offset voltage or low input offset current. The accuracy of long interval integrators, timers and sample and hold circuits is improved due to the low drift and low bias currents of the 101 A and 301 A. Frequency response may be matched to the individual circuit need with one external capacitor. The absence of "latch-up" coupled with internal short circuit protection make the 101 A, 201 A and 301 A virtually foolproof. The 101 A, 301 A are pin compatible with the popular μ A709, μ A741, μ A748 and μ A777.

FEATURES

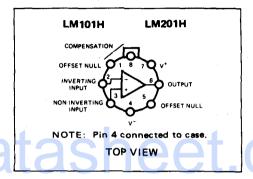
Low offset current and voltage. Low offset current drift. Low bias current. Short circuit protected. Low power consumption.

ABSOLUTE MAXIMUM RATINGS

ABSOLUTE MAXIMUM RATING				
Supply Voltage Military (101A) Commercial (301A)	±22V ±18V			
Internal Power Dissipation Metal Can DIP Mini DIP	500 mW 670 mW 310 mW			
Differential Input Voltage	±30V			
Input Voltage	±15V			
Storage Temperature Range Metal Can, DIP Mini DIP	-65°C to +150°C -55°C to +125°C			
Operating Temperature Range Military (101A) Commercial (301A)	-55°C to +125°C 0°C to +70°C			
Lead Temperature (Soldering) Metal Can, DIP (60 seconds) Mini DIP (10 seconds)	300°C 260°C			
Output Short Circuit Duration	Indefinite			
REFERENCE TABLE				
Code	Stock No.			
LM101AD LM101AH LM301AH LM301AN	35800 B 35801 X 35802 R 35803 G			

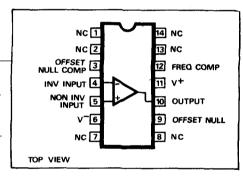
CONNECTION DIAGRAM

See outline drawing No 97 for dimensions.



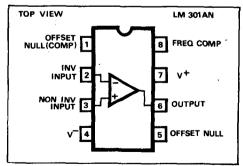
CONNECTION DIAGRAM

See outline drawing No 131 for dimensions.



CONNECTION DIAGRAM

See outline drawing No 132 for dimensions.



Linear I.C.'s - Operational Amplifiers

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LM301A Series Operational Amplifiers

REFERENCE TABLE

Code	Stock No.
LM301 AH	19661 C
LM301 AN	19752H

The LM301A is a general-purpose operational

GENERAL DESCRIPTION

amplifier which features improved performance over the 709C and other popular amplifiers. Advanced processing techniques make possible an order of magnitude reduction in input currents, and a redesign of the biasing circuitry reduces the temperature drift of input current. This amplifier offers many features which make its application nearly foolproof: overload protection on the input and output, no latch-up when the common mode range is exceeded, freedom from oscillations and compensation with a single 30pF capacitor. It has advantages over internally compensated amplifiers in that the compensation can be tailored to the particular application. For example, as a summing amplifier, slew rates of 10 V/µs and bandwidths of 10 MHz can be realised. In addition, the circuit can be used as a

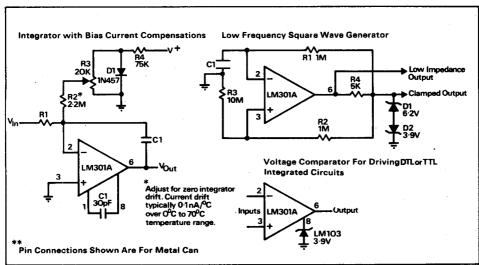
comparator with differential inputs up to $\pm 30V$; and the output can be clamped at any desired level

ABSOLUTE MAXIMUM RATINGS

to make it compatible with logic circuits.

Supply Voltage	±18V		
Power Dissipation (Note 1)			
Differential Input Voltage	±30V		
Input Voltage (Note 2)	±15V		
Output Short-Circuit Duration (Note 3)	Indefinite		
Operating Temperature Range	0°C to 70°C		
Storage Temperature Range	65°C to 150°C		
Lead Temperature (Soldering, 60 sec)	300°C		

TYPICAL APPLICATIONS* *

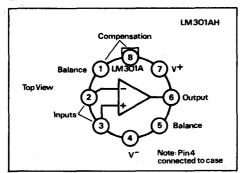


National Semiconductor

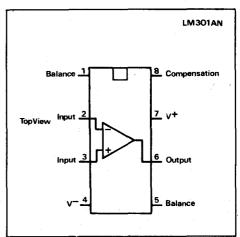
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Linear I.C.'s - Operational Amplifiers

CONNECTION DIAGRAMS



See outline drawing No. 97 for dimensions.



See outline drawing No. 103 for dimensions.

ELECTRICAL CHARACTERISTICS (Note 4)

Parameter	Conditions	Min.	Тур.	Max.	Units
Input Offset Voltage Input Offset Current Input Bias Current Input Resistance Supply Current	$T_A = 25^{\circ}C, R_S \le 50 \text{ k}\Omega$ $T_A = 25^{\circ}C$ $T_A = 25^{\circ}C$ $T_A = 25^{\circ}C$ $T_A = 25^{\circ}C, V_S = \pm 15V$	0.5	2.0 3 70 2 1.8	7.5 50 250	mV nA nA MΩ mA
Large Signal Voltage Gain	$T_A = 25^{\circ}C$, $V_S = \pm 15V$ $V_{OUT} = \pm 10V$, $R_L \ge 2 \text{ k}\Omega$	25	160		V/mV
Input Offset Voltage	$R_s \le 50 \text{ k}\Omega$			10	mV
Average Temperature Coefficient of Input Offset Voltage		-	6.0	30	μV/°C
Input Offset Current				70	nΑ
Average Temperature Coefficient of Input Offset Current	25°C ≤ T _A ≤ 70°C 0°C ≤ T _A ≤ 25°C		0.01 0.02	0.3 0.6	nA/°C
Input Bias Current		1		300	nA
Large Signal Voltage Gain	$V_s = \pm 15V, V_{OUT} = \pm 10V$ $R_L \ge 2 k\Omega$	15			V/mV
Output Voltage Swing	$V_{S}=\pm 15V,R_{L}=10k\Omega$ $R_{L}=2k\Omega$	±12 ±10	±14 ±13		V
Input Voltage Range	$V_s = \pm 15V$	±12			V.
Common Mode Rejection Ratio	$R_s \le 50 \text{ k}\Omega$	70	90		dB
Supply Voltage Rejection Ratio	$R_s \le 50 \text{ k}\Omega$	70	96		dB

Note 1: For operating at elevated temperatures, the device must be derated based on a 100°C maximum junction temperature and a thermal resistance of 150°C/W junction to ambient or 45°C/W junction to case.

Note 2: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

Note 3: Continuous short circuit is allowed for case temperatures to 70°C and ambient temperatures to

Note 4: These specifications apply for 0° C \leq T_A \leq 70°C, \pm 5V, \leq V_S \leq \pm 15V and C1 30pF unless otherwise specified.