BD243C and BD244C are Preferred Devices

Complementary Silicon Plastic Power Transistors

These devices are designed for use in general purpose amplifier and switching applications.

Features

- Collector Emitter Saturation Voltage –
 V_{CE(sat)} = 1.5 Vdc (Max) @ I_C = 6.0 Adc
- Collector Emitter Sustaining Voltage –
 V_{CEO(sus)} = 80 Vdc (Min) BD243B, BD244B
 = 100 Vdc (Min) BD243C, BD244C
- High Current Gain Bandwidth Product $f_T = 3.0 \text{ MHz} \text{ (Min)} @ I_C = 500 \text{ mAdc}$
- Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage BD243B, BD244B BD243C, BD244C	V _{CEO}	80 100	Vdc
Collector-Base Voltage BD243B, BD244B BD243C, BD244C	V _{CB}	80 100	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous – Peak	I _C	6 10	Adc
Base Current	I _B	2.0	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	65 0.52	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.92	°C/W

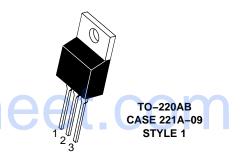
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



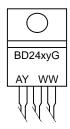
ON Semiconductor®

http://onsemi.com

6 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 80-100 VOLTS 65 WATTS



MARKING DIAGRAM



BD24xy = Device Code

x = 3 or 4y = B or C

A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

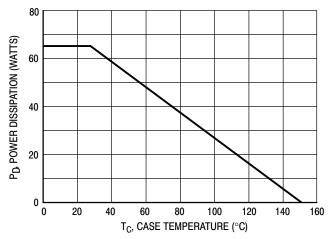


Figure 1. Power Derating

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (Note 1) (I _C = 30 mAdc, I _B = 0)	BD243B, BD244B BD243C, BD244C	V _{CEO(sus)}	80 100	_ _	Vdc
Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0)	BD243B, BD243C, BD244B, BD244C	I _{CEO}	-	0.7	mAdc
Collector Cutoff Current (V _{CE} = 80 Vdc, V _{EB} = 0) (V _{CE} = 100 Vdc, V _{EB} = 0)	BD243B, BD244B BD243C, BD244C	I _{CES}	- -	400 400	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}	_	1.0	mAdc

ON CHARACTERISTICS (Note 1)

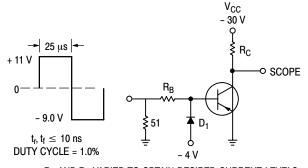
DC Current Gain	h _{FE}	30 15	- -	-
Collector–Emitter Saturation Voltage (I _C = 6.0 Adc, I _B = 1.0 Adc)	V _{CE(sat)}	-	1.5	Vdc
Base–Emitter On Voltage (I _C = 6.0 Adc, V _{CE} = 4.0 Vdc)	V _{BE(on)}	-	2.0	Vdc

DYNAMIC CHARACTERISTICS

Current-Gain - Bandwidth Product (Note 2) (I _C = 500 mAdc, V _{CE} = 10 Vdc, f _{test} = 1.0 MHz)	f _T	3.0	-	MHz
Small–Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	20	-	1

^{1.} Pulse Test: Pulsewidth $\leq 300 \,\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

^{2.} $f_T = h_{fe} \bullet f_{test}$



 R_B and R_C varied to obtain desired current levels D_1 must be fast recovery type eg. 1N5825 used above $I_B\approx 100$ ma MSD6100 used below $I_B\approx 100$ ma

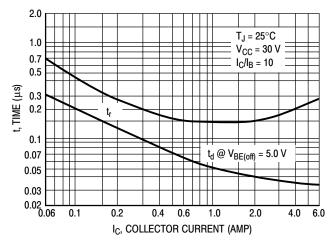


Figure 3. Turn-On Time



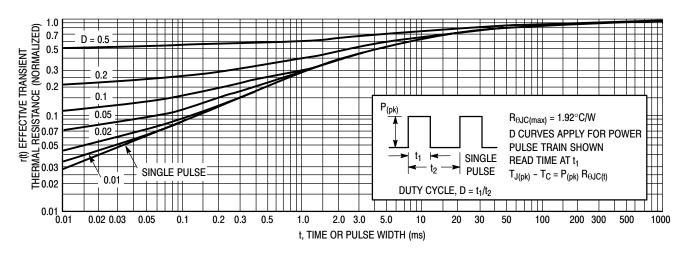


Figure 4. Thermal Response

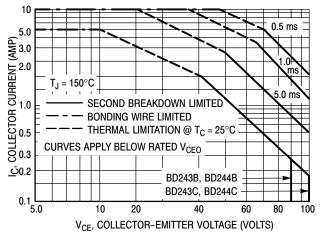


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}\text{C}$: T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^{\circ}\text{C}$, $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

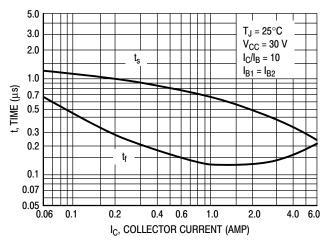


Figure 6. Turn-Off Time

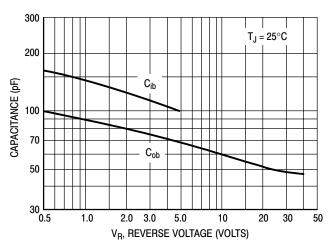


Figure 7. Capacitance

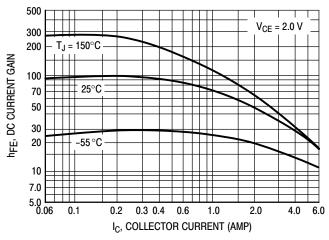


Figure 8. DC Current Gain

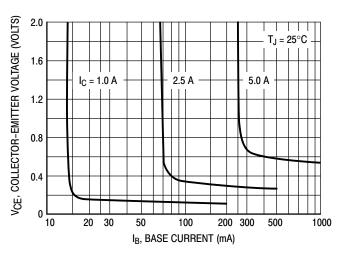


Figure 9. Collector Saturation Region

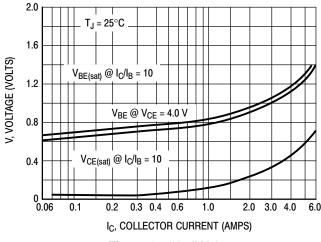


Figure 10. "On" Voltages

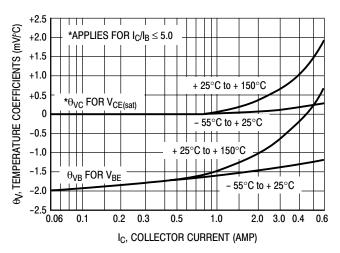
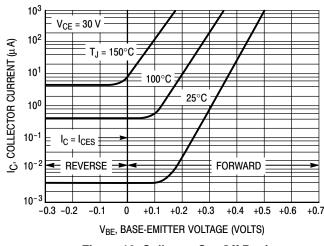
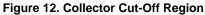


Figure 11. Temperature Coefficients





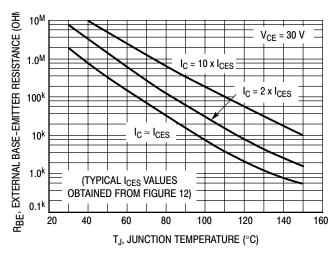


Figure 13. Effects of Base-Emitter Resistance

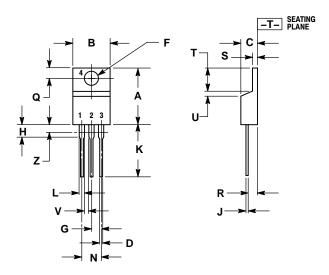
ORDERING INFORMATION

Device	Package	Shipping [†]
BD243B	TO-220	
BD243BG	TO-220 (Pb-Free)	50 Units / Rail
BD243C	TO-220	
BD243CG	TO-220 (Pb-Free)	50 Units / Rail
BD244B	TO-220	
BD244BG	TO-220 (Pb-Free)	50 Units / Rail
BD244C	TO-220	
BD244CG	TO-220 (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 **ISSUE AA**



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		INCHES MILI		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX		
Α	0.570	0.620	14.48	15.75		
В	0.380	0.405	9.66	10.28		
С	0.160	0.190	4.07	4.82		
D	0.025	0.035	0.64	0.88		
F	0.142	0.147	3.61	3.73		
G	0.095	0.105	2.42	2.66		
Н	0.110	0.155	2.80	3.93		
J	0.018	0.025	0.46	0.64		
K	0.500	0.562	12.70	14.27		
L	0.045	0.060	1.15	1.52		
N	0.190	0.210	4.83	5.33		
Q	0.100	0.120	2.54	3.04		
R	0.080	0.110	2.04	2.79		
S	0.045	0.055	1.15	1.39		
T	0.235	0.255	5.97	6.47		
U	0.000	0.050	0.00	1.27		
٧	0.045		1.15			
Z		0.080		2.04		

STYLE 1:

PIN 1. BASE

- 2. COLLECTOR
- 3. EMITTER
- 4. COLLECTOR

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